

WHAT IS CLAIMED IS:

1. A processing apparatus comprising:
 - a drive pulse generator circuit for generating a drive pulse to be supplied to an image pickup element; and
 - a wave form data supply circuit for supplying wave form setting data for generating the drive pulse to said drive pulse generator circuit at each horizontal line,
 - 10 wherein the wave form setting data includes a wave form setting data to be set at each horizontal line and wave form setting data sharing a setting area.
- 15 2. The processing apparatus according to claim 1, wherein the wave form setting data sharing the setting area includes wave form setting data and an identification code for identifying a type of the wave form setting data, and wherein said drive pulse generator circuit includes a first detection circuit which detects the wave form setting data with reference to a horizontal synchronization signal, and a second detection circuit which detects the wave form setting data in accordance with the identification code.
- 25 3. The processing apparatus according to claim

1, wherein the wave form setting data is sent from said wave form data supply circuit to said drive pulse generator circuit during a horizontal blanking period of the image pickup element.

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4. The processing apparatus according to claim 1, wherein said wave form data supply circuit includes a memory which stores wave form setting data.

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5. The processing apparatus according to claim 1, wherein said wave form data supply circuit switches among a plurality of modes including a moving image mode, a still image mode and a monitor mode.

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6. The processing apparatus according to claim 1, wherein said wave form data supply circuit is integrated in a signal processing circuit for processing image data supplied from the image pickup element.

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